

IDS - 02/04/2014 **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	IC DESIGN MODELING ALLOWING DIMENSION-DEPENDENT RULE CHECKING																																		
<p>Application Number : 10/708,039</p> <p>Confirmation Number:</p> <p>First Named Applicant: Evanthia Papadopoulou</p> <p>Attorney Docket Number: BUR920030149US1</p> <p>Art Unit: 2825</p> <p>Examiner: Suchin Parihar</p> <p>Search string: (6317859 or 6178539).pn</p> <p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>SP</td><td>1</td><td>6317859</td><td>2001-11-13</td><td>Papadopoulou</td><td></td><td></td><td></td></tr><tr><td>SP</td><td>2</td><td>6178539</td><td>2001-01-23</td><td>Papadopoulou et al.</td><td></td><td></td><td></td></tr></tbody></table> <p>Signature</p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td>/Suchin Parihar/</td><td>12/18/2006</td></tr></tbody></table>								init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	SP	1	6317859	2001-11-13	Papadopoulou				SP	2	6178539	2001-01-23	Papadopoulou et al.				Examiner Name	Date	/Suchin Parihar/	12/18/2006
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